

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. <b>310114US2PCT</b>		SERIAL NO. <b>10/551,702</b>	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT <b>Haruki TODA</b>			
				FILING DATE <b>September 30, 2005</b>		GROUP <b>2815</b>	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
<b>FOREIGN PATENT DOCUMENTS</b>							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES                      NO		
	AO						
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
<b>OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
	AW	ISSCC 2003 / SESSION 16 / NON-VOLATILE MEMORY / PAPER 16.4; "512Mb PROM with 8 Layers of Antifuse/Diode Cells" Matthew Crowley et al.					
	AX	IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol. 38, No. 11, November 2003, pp. 1920-1928, "512-Mb PROM With a Three-Dimensional Array of Diode/Antifuse Memory Cells", Mark Johnson et al.					
	AY						
	AZ					<input type="checkbox"/> Additional References sheet(s) attached	
Examiner					Date Considered		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							